

COLLEGE: GOA COLLEGE OF ENGINEERING Seat No: 3101 PRNo: 201507401 Sex: M Name: BHASALE VINAY BHANUDAS No Of Attempts: 1 No Of Grade **SGPA** Credits Obtained Solid State Devices and Semiconductor Physics Theory BB 2 BB Digital VLSI Design Theory BC IA 2 ВВ Analog VLSI Design Theory CC AB VLSI Technology Theory 4 BB 2 BB Advanced Engineering Mathematics Theory 4 FF IA 2 HDL - LAB IA 2 AB Practical 2 VLSI CAD - LAB 2 IA BB P Practical 2 ВВ Ρ Total: 38 6.05 F

						FAILS	
Seat No: 3102	PRNo: 201107681	Se	x: M	Nan	ne :	BORKAR NEEL RAMAKAN	T
No Of Attempts: 1	v		No Of Credits	Gra Obta	de	SGPA	
Solid State De	evices and Semiconductor	Physics	o rouno	05.0			2
		Theory	4	BB	Ρ		
		IA	2	AB	Р		
Digital VLSI D	esign						
		Theory	4	CC	Ρ		
		IA .	2	AB	P		
Analog VLSI [Design						
		Theory	4	AB	Р		
		IA	2	AB	Р		
VLSI Technol	ogy						
		Theory	4	AB	Р		
		IA	2	AB	P		39
Advanced Eng	gineering Mathematics	•					
		Theory	4	FF	F		
		IA	2	BC	P		
HDL - LAB							
		IA	2	AB	Ρ		
		Practical	2	BB	Р		
VLSI CAD - LA	AB						
		IA	2	BB	P		
*	7	Practical	2	BC	Р		
		Total:	38	******************	*********	6.42 F FAILS	



[.] P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average

COLLEGE: GOA COLLEGE OF ENGINEERING

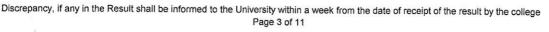
at No: 3103 PR No: 2011083	81 Se	ex: F	Na	me :	GOVEKAR DIVYA DIGAMBAR	
Of Attempts: 1		No Of	Gr	ade		
Solid State Devices and Semiconduc	tor Physics	Credits	Obt	ained	SGPA	
	Theory	4	CC	Р		
	IA	2	BB			
Digital VLSI Design		_	55			
	Theory	4	FF	F		
	IA	2	CC	Р		
Analog VLSI Design						
	Theory	4	FF	F		
VLSI Technology	IA	2	BB	Р		
VEOI Technology	Thomas		-	_		
	Theory IA	4	CC	Р	a .	
Advanced Engineering Mathematics	10	. 2	ВС	Р	*	
	Theory	4	FF	F		
	IA	2	ВС	P		
HDL - LAB		and.		0(*0)		
	IA	2	AA	Р		
	Practical	2	AB	P		
VLSI CAD - LAB						
	IA	2	BB	Р		
	Practical	2	BC	P		
	Designation of the latest of t		THE PERSON NAMED IN COLUMN 2 IN COLUMN 2			
	Total:	38			4.26 F	********
t No : 3104 P.R.No : 20100630			Non		FAILS	
t No : 3104 P R No : 20100630		c: F			4.26 F FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1	5 Sex	c: F No Of	Gra	de	FAILS KALSHAONKAR REEMA DATTARAM	
	5 Sex	c: F		de	FAILS	
Of Attempts: 1	5 Sex	c: F No Of	Gra	de	FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1 Solid State Devices and Semiconducte	5 Sex	C: F No Of Credits	Gra Obta	de ined	FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1	5 Sex or Physics Theory IA	C: F No Of Credits	Gra Obta BB AA	de ined P	FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1 Solid State Devices and Semiconducte	or Physics Theory IA Theory	No Of Credits 4 2	Gradobtal Obtal BB AA BC	de ined P P	FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1 Solid State Devices and Semiconductor Digital VLSI Design	5 Sex or Physics Theory IA	No Of Credits 4 2	Gra Obta BB AA	de ined P P	FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1 Solid State Devices and Semiconducte	or Physics Theory IA Theory IA	No Of Credits 4 2 4 2	Gradobtai BB AA BC BC	de ined P P P	FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1 Solid State Devices and Semiconductor Digital VLSI Design	or Physics Theory IA Theory IA Theory	No Of Credits 4 2 4 2	Gradobtai BB AA BC BC	de ined P P P	FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design	or Physics Theory IA Theory IA	No Of Credits 4 2 4 2	Gradobtai BB AA BC BC	de ined P P P	FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1 Solid State Devices and Semiconductor Digital VLSI Design	or Physics Theory IA Theory IA Theory IA	No Of Credits 4 2 4 2	Gra Obta BB AA BC BC AA AA	de ined P P P P	FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design	or Physics Theory IA Theory IA Theory IA Theory IA	No Of Credits 4 2 4 2 4 2	Gra Obta BB AA BC BC AA AA	de ined P P P P P	FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design	or Physics Theory IA Theory IA Theory IA	No Of Credits 4 2 4 2	Gra Obta BB AA BC BC AA AA	de ined P P P P	FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology	or Physics Theory IA Theory IA Theory IA Theory IA	No Of Credits 4 2 4 2 4 2 4 2	BB AA BC BC AA AA AA	de ined P P P P P P	FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics	or Physics Theory IA Theory IA Theory IA Theory IA Theory IA	No Of Credits 4 2 4 2 4 2	Gra Obta BB AA BC BC AA AA	de ined P P P P P	FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology	or Physics Theory IA Theory IA Theory IA Theory IA Theory IA Theory IA	No Of Credits 4 2 4 2 4 2 4 2 4	BB AA BC BC AA AA AA BC BC	de ined PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics	or Physics Theory IA	No Of Credits 4 2 4 2 4 2 4 2 4	BB AA BC BC AA AA AA BC BC	de ined PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics HDL - LAB	or Physics Theory IA Theory IA Theory IA Theory IA Theory IA Theory IA	No Of Credits 4 2 4 2 4 2 4 2 4 2	BB AA BC AA AA AA AA BC AB	de ined PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics	or Physics Theory IA Theory IA Theory IA Theory IA Theory IA Theory IA IA Practical	No Of Credits 4 2 4 2 4 2 4 2 4 2 4 2	BB AA BC BC AA	de ined PP PP PP PP PP	FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics HDL - LAB	or Physics Theory IA Theory IA Theory IA Theory IA Theory IA IA Theory IA	No Of Credits 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4	BB AA BC BC AA AA AA AA BC AB AO AO BB	de ined PP PP PP PP PP PP PP	FAILS KALSHAONKAR REEMA DATTARAM	
Of Attempts: 1 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics HDL - LAB	or Physics Theory IA Theory IA Theory IA Theory IA Theory IA Theory IA IA Practical	No Of Credits 4 2 4 2 4 2 4 2 4 2 4 2	BB AA BC BC AA	de ined PP PP PP PP PP	FAILS KALSHAONKAR REEMA DATTARAM	

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average





COLLEGE: GOA COLLEGE OF ENGINEERING Seat No: 3105 PR No: 200907827 Sex: F Name: KHANDEPARKAR SHWETA MAHENDRA No Of Attempts: 1 No Of Grade **SGPA** Credits Obtained Solid State Devices and Semiconductor Physics Theory 4 AB P IA 2 AB P Digital VLSI Design Theory 4 AB P IA 2 AB P Analog VLSI Design Theory 4 BB P IA 2 AB P VLSI Technology Theory 4 BB P IA 2 AB P Advanced Engineering Mathematics Theory 4 BB P IA 2 AB P HDL - LAB 2 IA AA P Practical 2 AA Р VLSI CAD - LAB IA 2 P AB Practical Ρ 2 BB Total: 38 7.74 P **PASSES** PRNo: 201008499 Sex: F Name: KHARANGATE LAXIMI SHASHANK No Of Grade **SGPA** Credits Obtained Solid State Devices and Semiconductor Physics Theory 4 AB P 2 AA P Digital VLSI Design Theory 4 BC P IA 2 AB P Analog VLSI Design Theory 4 AB P 2 AB P VLSI Technology Theory AB P IA AA P Advanced Engineering Mathematics Theory BC BB P HDL - LAB





Seat No: 3106 No Of Attempts: 1 IA 2 Р AA Practical 2 Р AB VLSI CAD - LAB 2 Ρ AB Practical 2 AB Ρ Total: 38 7.68 P



COLLEGE:	GOA COLLEG	E OF ENGINEERING
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eat No : 3107		201105539	Se	ex: M	Nan	ne:	KORDE CHARUDATTA GURUDAS	
o Of Attempts: 1				No Of Credits	Gra		SGPA	
Solid State	Devices and S	Semiconductor F	Physics	Credits	Obta	inea	331 /X	
			Theory	4	ВС	Р		
			IA	2	AB	Р		
Digital VLS	I Design		1/1	2	AD.			
Digital VEC	Doolgii		Theory	4	вс	Р	, · · · · · · · · · · · · · · · · · · ·	
			IA	2				
Analog VL	SI Docion		IA	2	AB	Ρ		
Analog VL	of Design		Th		200	_		
			Theory	4	BC	Р		
VI CI Took			IA	2	AA	Ρ		
VLSI Techi	lology			10.00				
			Theory	4	BC	Р		
		7010 POE	IA	2	BB	Р		
Advanced I	Engineering Ma	thematics						
			Theory	4	CC	P		
			IA	2	BC	P		
HDL - LAB								
			IA	2	AA	P	3*	
	20		Practical	2	AO	P		
VLSI CAD	LAB						×	
			IA	2	AA	Р		
			Practical	2	AA	Р		
			Total:	38			7.00 P	
				-			PASSES	
at No: 3108	PRNo:	201107707	Se	x : M	Nam	e:	MAHATME MOHIT BHAIRAV	
Of Attempts: 1				No Of	Grad			
				Credits	Obtai		SGPA	
Solid State	Devices and Se	emiconductor P	hysics					
			Theory	4	BC	P		
			157					
D: :: 11 # 0			IA	2	BB	Р		
Digital VLS	Design		IA	2	ВВ	Р		
Digital VLS	Design							
Digital VLS	Design		Theory	4	CC	Р		
Analog VLS			Theory IA	4 2	CC	P P		
			Theory IA Theory	4 2 4	CC CC BC	P P		
Analog VLS	il Design		Theory IA	4 2	CC	P P		
	il Design		Theory IA Theory IA	4 2 4 2	CC CC BC BC	P P P		
Analog VLS	il Design		Theory IA Theory IA	4 2 4 2	CC CC BC BC	P P P P		
Analog VLS	il Design ology		Theory IA Theory IA	4 2 4 2	CC CC BC BC	P P P		
Analog VLS	il Design	thematics	Theory IA Theory IA Theory IA	4 2 4 2 4 2	CC CC BC BC FF BB	P P P F P		
Analog VLS	il Design ology	thematics	Theory IA Theory IA Theory IA Theory	4 2 4 2 4 2	CC CC BC BC FF BB	P P P P P		
Analog VLS VLSI Techn Advanced E	il Design ology	thematics	Theory IA Theory IA Theory IA	4 2 4 2 4 2	CC CC BC BC FF BB	P P P F P		
Analog VLS	il Design ology	thematics	Theory IA Theory IA Theory IA Theory IA	4 2 4 2 4 2	CC CC BC BC FF BB	PP PP FP PP		
Analog VLS VLSI Techn Advanced E	il Design ology	thematics	Theory IA Theory IA Theory IA Theory IA	4 2 4 2 4 2	CC CC BC BC FF BB	P P P P P		
Analog VLS VLSI Techn Advanced E	ology ingineering Mar	thematics	Theory IA Theory IA Theory IA Theory IA	4 2 4 2 4 2	CC CC BC BC FF BB	PP PP FP PP		
Analog VLS VLSI Techn Advanced E	ology ingineering Mar	thematics	Theory IA Theory IA Theory IA Theory IA	4 2 4 2 4 2 2	CC CC BC BC BC CC BC	PP PP FP PP		
Analog VLS VLSI Techn Advanced E	ology ingineering Mar	thematics	Theory IA Theory IA Theory IA Theory IA	4 2 4 2 4 2 2	CC CC BC BC BC CC BC	PP PP FP PP		

Total:

38

5.63 F FAILS





COLLEGE:	GOA COLLEGE	OF ENGINEERING
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		Total:	38			4.47 F FAILS	TO P STATE OF THE PARTY.
		Practical	2	CC	Р		
		IA	2	BC	Ρ		
VLSI CAD - LA	∖B						
		Practical	2	BB	Р		
		IA	2	AB	Ρ		
HDL - LAB							
		IA	2	CC	Ρ		
		Theory	4	FF	F		
Advanced Eng	ineering Mathematics						
		IA	2	CC	P		
		Theory	4		P		
VLSI Technolo	pgy						
		IA	2	BC	P		
120		Theory	4	FF	F		
Analog VLSI [Design						
		IA	2	CC	Ρ		
		Theory	4	CC	Р		
Digital VLSI D	esign					*	
		IA	2	BC	Р		
		Theory	4	BC	P		
Solid State De	vices and Semiconductor	Physics			0.5		
			Credits	Obta		SGPA	
No Of Attempts: 1			No Of	Gra	do		
Seat No: 3109	PRNo: 201007353	Sex	x: F	Nan	ne:	MAYEKAR MITHILI NARAYAN	

Seat No: 3110	PRNo: 201106308	Se	x: F	Nam	e:	NAIK STHRIGDHARA NAVANATH	
No Of Attempts: 1		DI :	No Of Credits	Grad Obtai		SGPA	
Solid State De	vices and Semiconductor						
		Theory	4	BC	Р		
		IA	2	AB	P		
Digital VLSI D	esign						
		Theory	4	AB	P		
		IA	2	BC	P		
Analog VLSI	Design						
= 3		Theory	4	CC	Ρ		
		IA	2	ВВ	Р		
VLSI Technolo	oav	att of	_				
	0,	Theory	4	ВС	Р		
		IA	2	BB	Р		83
Advanced End	ineering Mathematics		_				
, availoda Elig	mooning mathematics	Theory	4	FF	F		
		IA	2	CC	P		
HDL - LAB		1/2	2	00	Г		
TIDE - LAD		LA	•	DD.	_		
		IA	2	BB	Р		
	-	Practical	2	BB	Ρ		
VLSI CAD - LA	/R		1000	700 santasa	2000	¥' :	
		IA	2	BB	Ρ		
		Practical	2	ВС	Р		
		Total:	38			5.79 F FAILS	



COLLEGE: GOA COLLEGE OF ENGINEERING Seat No: 3111 PRNo: 201008518 Sex: F Name: NAIK TEJA UDAY No Of Attempts: 1 No Of Grade **SGPA** Credits Obtained Solid State Devices and Semiconductor Physics Theory 4 AB P IA 2 AB Ρ Digital VLSI Design Theory 4 BC Р IA 2 BB Р Analog VLSI Design Theory 4 BB P IA 2 BB P VLSI Technology Theory 4 AB P IA 2 AA P Advanced Engineering Mathematics Theory 4 CC Ρ Ρ IA 2 CC HDL - LAB 2 P IA BB Practical P 2 BC VLSI CAD - LAB IA 2 P BB Practical Р 2 BC Total: 38 6.84 P PASSES Seat No: 3112 PRNo: 201107719 Sex: M Name: PATIL ABHIJEET VILAS No Of Attempts: 1 No Of Grade **SGPA** Credits Obtained Solid State Devices and Semiconductor Physics Theory 4 FF 2 BC P Digital VLSI Design Theory 4 FF F 2 CC P Analog VLSI Design Theory 4 F 2 BC P VLSI Technology Theory 4 CC P 2 CC P Advanced Engineering Mathematics Theory 4 F 2 CC P HDL - LAB 2 AB P Practical 2 BB P VLSI CAD - LAB

2

2

38

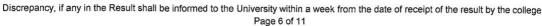
BB P

CC P

3.37 F FAILS

Practical

Total:





P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average

RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION HELD IN NOVEMBER 2015

Course : Revised Course - 2013

Seat No: 3113 PR No: 201105565			NI-		DANE KARAN RABAN
No Of Attempts: 1	, 56	ex: M			RANE KARAN PARSHURAM
		No Of Credits		ade ained	SGPA
Solid State Devices and Semiconducto	r Physics	Oredits	Obla	anieu	SGIA
	Theory	4	BB	Р	
	IA	2	AB	Р	
Digital VLSI Design					
	Theory	4	CC	Р	
	IA	2	BB	Р	
Analog VLSI Design					
	Theory	4	AA	Р	
VLSI Technology	IA	2	AO	P	
VESI recinology	T I.	W.#			
	Theory	4	AB	Р	
Advanced Engineering Mathematics	IA	2	BB	Ρ	
Advanced Engineering Wathernatics	Theory	4		_	
	IA	4 2	BB AB	Р	
HDL - LAB	in.	2	Ab	Ρ	
	IA	2	AA	Р	
	Practical	2	AA	Р	
VLSI CAD - LAB		1. 51 .0	, , ,	•	
	IA	. 2	AA	Р	
ži.	Practical	2	AA	Р	, a, i
	Total:	38			7.79 P
	***************************************				PASSES
eat No : 3114 P.R.No : 201106312	Sex	x: M	Nam	ne: S	SAWAL SAGAR NAMDEV
o Of Attempts: 1		No Of	Grad	de	
Solid State Devices and Semiconductor	Dh:-	Credits	Obtai	ned	SGPA
Cond State Devices and Semiconductor	Physics				
	Thoons		-	_	
	Theory	4	BB	Р	
	Theory IA	4 2	BB BB	P P	
Digital VLSI Design	IA	2	ВВ	Р	
	IA Theory	2	BB BB	P P	
	IA	2	ВВ	Р	
Digital VLSI Design	IA Theory IA	2 4 2	BB BB BC	P P	
Digital VLSI Design	IA Theory	2	BB BB BC BC	P P P	
Digital VLSI Design	IA Theory IA Theory	2 4 2 4	BB BB BC	P P	
Digital VLSI Design Analog VLSI Design	IA Theory IA Theory	2 4 2 4	BB BB BC BC	P P P	
Digital VLSI Design Analog VLSI Design VLSI Technology	IA Theory IA Theory IA	2 4 2 4 2	BB BC BC BB	P P P	
Digital VLSI Design Analog VLSI Design	IA Theory IA Theory IA Theory	2 4 2 4 2	BB BB BC BB BB	P P P P	
Digital VLSI Design Analog VLSI Design VLSI Technology	IA Theory IA Theory IA Theory	2 4 2 4 2	BB BB BC BB BB	P P P P	
Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics	IA Theory IA Theory IA Theory IA	2 4 2 4 2 4 2	BB BB BC BB BB BB BB BC	P P P P P	
Digital VLSI Design Analog VLSI Design VLSI Technology	Theory IA Theory IA Theory IA Theory IA	2 4 2 4 2 4 2	BB BB BC BB BB BC BB BC	P P P P P	
Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics	Theory IA Theory IA Theory IA Theory IA Theory IA	2 4 2 4 2 4 2	BB BB BC BB BC CC AB	P P P P P	
Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics HDL - LAB	Theory IA Theory IA Theory IA Theory IA	2 4 2 4 2 4 2	BB BB BC BB BC CC	P	
Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics	Theory IA Theory IA Theory IA Theory IA Theory IA Theory IA	2 4 2 4 2 4 2 2 2 2	BB BB BC BB BC CC AB AB	P P P P P P P P P P P P P P P P P P P	
Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics HDL - LAB	Theory IA Theory IA Theory IA Theory IA Theory IA IA IA	2 4 2 4 2 4 2 2 2 2	BB BB BC BB BC CC AB AB AA	P P P P P P P P P	
Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics HDL - LAB	Theory IA Theory IA Theory IA Theory IA Theory IA Theory IA	2 4 2 4 2 4 2 2 2 2	BB BB BC BB BC CC AB AB	P P P P P P P P P P P P P P P P P P P	

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; +: Grades Carried Over; SGPA: Semester Grade Point Average; GGPA: Cummulative Grade Point Average



COLLEGE: GOA COLLEGE OF ENGINEERING

			Total:	38			2.79 F	
			Practical		BC	Р		
VEGI CAD - EA			IA	2	ВВ	Р		
VLSI CAD - LA	R		ractical	2	DD	F		
			Practical	2 2	BB BB	P P		
HDL - LAB			IA	•	DD	_		
ארו ועם			IA	2	CC	Р		
			Theory	4	FF	F		
Advanced Eng	neering Mat	hematics			-			
			IA	2	CC	Ρ		
			Theory	4	FF	F		
VLSI Technolo	gy							
			IA	2	CC	Р		
			Theory	4	FF	F		
Analog VLSI D	esign							
			IA	2	CC	Ρ		
			Theory	4	FF	F		
Digital VLSI De	esign							
			IA	2	ВС	Р	(4)	
			Theory	4	FF	F		
Solid State De	vices and Se	emiconductor I	Physics	Credits	Obta	inea	JGI A	
TO OI Allempia.				No Of	Gra		SGPA	
No Of Attempts: 1				ex: M			SHETGAONKAR ANAY ANIL	

	PR No: 200806775	Sex:	M	Nam	e:	ESTEBEIRO SHELTON PIETY
No Of Attempts: 4	· ·		No Of Credits	Grad Obtai		SGPA
Solid State Devic	es and Semiconductor Phys					
	Th	eory	4	CC	+	
	IA		2	CC	+	
Digital VLSI Design	gn -					
	Th	eory	4	CC	+	
	IA		2	BB	+	
. Analog VLSI Des	ign					
	Th	eory	4	CC	+	
	IA		2	CC	+	180
VLSI Technology						
	Th	eory	4	BB	+	
	IA		2	BB	+	
Advanced Engine	ering Mathematics					
	Th	eory	4	CC	Ρ	
	IA		2	BC	+	
HDL - LAB						
	IA		2	BB	+	
	Pra	actical	2	AB	+	
VLSI CAD - LAB						
	IA		2	BC	+	
	Pra	actical	2	AB	+	
	To	tal:	38			5.95 P PASSES

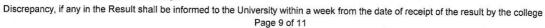
P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average



COLLEGE: GOA COLLEGE OF ENGINEERING

at No: 3117	PRNo: 2	01004714 S	ex: F	Nan	ne:	KANADE SHRADDHA SABAJI
Of Attempts: 2			No Of	Gra		SGPA
Solid State D	evices and Sem	iconductor Physics	Credits	Obta	ined	SGPA
	ovideo and oom	Theory	4	CC	+	
		IA	2	AA	+	
Digital VLSI I	Design		-	, , ,		
		Theory	4	ВС	+	
		IA	2	ВВ	+	
Analog VLSI	Design					
		Theory	4	BC	+	
		IA	2	BB	+	
VLSI Techno	ogy					
		Theory	4	CC	+	
		IA	2	AB	+	
Advanced En	gineering Mathe					
		Theory	4	FF	F	
HDL - LAB		IA	2	CC	+	
HUL - LAD		IA	2	Α Λ		
		Practical	2	AA	+	
VLSI CAD - L	AB	Fractical	4	AA	+	
		IA	2	AB	+	
		Practical		AB	+	
		Total:			-	600 F
		Total:	38		- Domeston	6.00 F FAILS
t No : 3118	PR No: 20			Nam	e :	6.00 F FAILS NAIK MILIND MINANATH
t No: 3118 Of Attempts: 3	PR No: 20		38 ·			FAILS
Of Attempts: 3		01301154 Se	38	Nam Grad Obtai	de	FAILS
Of Attempts: 3		01301154 Se	38 ex: M No Of Credits	Grad Obtai	de ned	FAILS NAIK MILIND MINANATH
Of Attempts: 3		01301154 Se conductor Physics Theory	38 ex: M No Of Credits 4	Grad Obtai BC	de ned +	FAILS NAIK MILIND MINANATH
Of Attempts: 3 Solid State De	evices and Semi	01301154 Se	38 ex: M No Of Credits	Grad Obtai	de ned +	FAILS NAIK MILIND MINANATH
Of Attempts: 3	evices and Semi	conductor Physics Theory IA	38 ex: M No Of Credits 4 2	Grad Obtain BC CC	de ned + +	FAILS NAIK MILIND MINANATH
Of Attempts: 3 Solid State De	evices and Semi	conductor Physics Theory IA Theory	38 ex: M No Of Credits 4 2	Grad Obtain BC CC	de ned + +	FAILS NAIK MILIND MINANATH
Of Attempts: 3 Solid State De	evices and Semi	conductor Physics Theory IA	38 ex: M No Of Credits 4 2	Grad Obtain BC CC	de ned + +	FAILS NAIK MILIND MINANATH
Of Attempts: 3 Solid State De	evices and Semi	conductor Physics Theory IA Theory IA	38 No Of Credits 4 2 4 2	Grac Obtain BC CC CC AB	de ned + + +	FAILS NAIK MILIND MINANATH
Of Attempts: 3 Solid State De	evices and Semi	conductor Physics Theory IA Theory IA Theory	38 No Of Credits 4 2 4 2	Grac Obtain BC CC CC AB	te ned + + +	FAILS NAIK MILIND MINANATH
Of Attempts: 3 Solid State De Digital VLSI D Analog VLSI I	evices and Semi esign Design	conductor Physics Theory IA Theory IA	38 No Of Credits 4 2 4 2	Grac Obtain BC CC CC AB	de ned + + +	FAILS NAIK MILIND MINANATH
Of Attempts: 3 Solid State De	evices and Semi esign Design	conductor Physics Theory IA Theory IA Theory IA	38 No Of Credits 4 2 4 2	Grac Obtain BC CC CC AB	te ned + + + + +	FAILS NAIK MILIND MINANATH
Of Attempts: 3 Solid State De Digital VLSI D Analog VLSI I	evices and Semi esign Design	conductor Physics Theory IA Theory IA Theory IA Theory IA Theory	38 No Of Credits 4 2 4 2 4 2	Grac Obtain BC CC CC AB CC CC	de ned + + + + + + + + + + + + + + + + + + +	FAILS NAIK MILIND MINANATH
Of Attempts: 3 Solid State De Digital VLSI D Analog VLSI I VLSI Technological	evices and Semi esign Design	conductor Physics Theory IA Theory IA Theory IA Theory IA Theory IA	38 No Of Credits 4 2 4 2	Grac Obtain BC CC CC AB	te ned + + + + +	FAILS NAIK MILIND MINANATH
Of Attempts: 3 Solid State De Digital VLSI D Analog VLSI I VLSI Technological	evices and Semi esign Design	conductor Physics Theory IA Theory IA Theory IA Theory IA Theory IA	38 No Of Credits 4 2 4 2 4 2	Grac Obtain BC CC AB CC CC	de ned + + + + + + + + + + + + + + + + + + +	FAILS NAIK MILIND MINANATH
Of Attempts: 3 Solid State De Digital VLSI D Analog VLSI I VLSI Technological	evices and Semi esign Design	conductor Physics Theory IA Theory IA Theory IA Theory IA Theory IA	38 No Of Credits 4 2 4 2 4 2	Grac Obtain BC CC CC AB CC CC	de ned + + + + + + + + + + + + + + + + + + +	FAILS NAIK MILIND MINANATH
Of Attempts: 3 Solid State De Digital VLSI D Analog VLSI I VLSI Technological	evices and Semi esign Design	conductor Physics Theory IA Theory	38 No Of Credits 4 2 4 2 4 2 4 2 4 4 4 4 4 4 4 4 4 4 4	Grac Obtain BC CC CC AB CC CC	de ned + + + + + + + + + + + + + + + + + + +	FAILS NAIK MILIND MINANATH
Of Attempts: 3 Solid State De Digital VLSI D Analog VLSI I VLSI Technolo Advanced Eng	evices and Semi esign Design	conductor Physics Theory IA Theory	38 No Of Credits 4 2 4 2 4 2 4 2 4 4 4 4 4 4 4 4 4 4 4	Grac Obtain BC CC CC AB CC CC	de ned + + + + + + + + + + + + + + + + + + +	FAILS NAIK MILIND MINANATH
Of Attempts: 3 Solid State Do Digital VLSI D Analog VLSI I VLSI Technolo Advanced Eng	evices and Semi esign Design ogy	conductor Physics Theory IA	38 No Of Credits 4 2 4 2 4 2 4 2 4 2	Grace Obtain BC CC CC AB CC	de ned + + + + + + + + + + + + + + + + + + +	FAILS NAIK MILIND MINANATH
Of Attempts: 3 Solid State De Digital VLSI D Analog VLSI I VLSI Technolo Advanced Eng	evices and Semi esign Design ogy	conductor Physics Theory IA	38 No Of Credits 4 2 4 2 4 2 4 2 4 2	Grac Obtain BC CC CC AB CC CC CC	de ned + + + + + + + + + + + + + + + + + + +	FAILS NAIK MILIND MINANATH
Of Attempts: 3 Solid State Do Digital VLSI D Analog VLSI I VLSI Technolo Advanced Eng	evices and Semi esign Design ogy	conductor Physics Theory IA Theory IA Theory IA Theory IA Theory IA Theory IA IA Theory IA IA IA IA Practical	38 Pax: M No Of Credits 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4	Grace Obtain BC CC AB CC	de ned + + + + + + + + + + + + + + + + + + +	FAILS NAIK MILIND MINANATH
Of Attempts: 3 Solid State Do Digital VLSI D Analog VLSI I VLSI Technolo Advanced Eng	evices and Semi esign Design ogy	conductor Physics Theory IA Theory IA Theory IA Theory IA Theory IA Theory IA IA Theory IA Theory IA Theory IA Theory IA	38 No Of Credits 4 2 4 2 4 2 4 2 4 2 4 2	Grace Obtain BC CC AB CC	de ned + + + + + + + + + + + + + + + + + + +	FAILS NAIK MILIND MINANATH

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average







VLSI CAD - LAB

RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION HELD IN NOVEMBER 2015 Course: Revised Course - 2013

COLLEGE: GOA COLLEGE OF ENGINEERING PR No: 200800872 Name: NORONHA ALWIN Sex: M Seat No: 3119 No Of Attempts: 2 No Of Grade **SGPA** Credits Obtained Solid State Devices and Semiconductor Physics CC Р Theory 4 2 BB IA Digital VLSI Design FF F 4 Theory 2 BB IA Analog VLSI Design Ρ CC Theory 4 BB 2 IA VLSI Technology CC P 4 Theory 2 AB IA Advanced Engineering Mathematics FF F 4 Theory 2 CC IA HDL - LAB 2 BB IA Practical 2 BC

2

2

IA Practical AB +

AB

		Total:	38			4.89 F FAILS
Seat No : 3120	PRNo: 200806910	Sex	c: M	Nam	e: S	ATARDEKAR GIRISH GURUDAS
No Of Attempts: 2	×		No Of Credits	Grad Obtai		SGPA
Solid State D	Devices and Semiconductor	Physics	,			
10		Theory	4	BC	+	
		IA	2	BC	+	
Digital VLSI	Design					
155		Theory	4	BB	+	
		IA	2	BB	+	
Analog VLSI	Design					
		Theory	4	BC	+	7 7 -
		IA	2 .	BB	+	
VLSI Techno	ology					
		Theory	4	BC	P	
		IA	2	AB	+	
Advanced E	ngineering Mathematics					
		Theory	4	FF	F	
		IA	2	CC	+	
HDL - LAB						
		IA	2	BC	+	⊙
		Practical	2	BC	+	
VLSI CAD -	LAB					
		IA	2	AB	+	
		Practical	2	BB	+	
		Total:	38		******************	5.79 F FAILS

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average



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COLLEGE: GOA COLLEGE OF ENGINEERING

* *		IA Practica	2		+ +		
VLSI CAD	- LAB		-	, , ,	5		
		Practica		AA	+		
-		IA	2	AB	+		
HDL - LAE	3		_	ΛD	T		
		IA	2		+		
	goog Wat	Theory	4	ВС	Р		
Advanced	Engineering Mat		2	AB	+		
		Theory _. IA	4 2	CC			
VEOI 160	inology	Thoons			50.		
VLSI Tecl	anology	IA	2	AB	+		
		Theory	4	BC	+		
Analog VI	_SI Design	-	20.50				
Analog VI	CI Davis	IA	2	AB	+		
		Theory		AA	+		(8)
Digital VL	.SI Design					1 6	
		IA	2	BB	+		
		Theory	4	CC	+		
Solid Sta	te Devices and S	emiconductor Physics	Orcans	Obtai	neu	33.7.	
			No Of Credits	Grad Obtai		SGPA	
No Of Attempts:	2		Sex: M			TRAVASSO VALERIUS SIRUS	

Grade	Grade Points	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
ВВ	7	Good
ВС	6	Fair
CC	5	Satisfactory
FF	0	Fail

S.S.J. Figueiredo Assistant Registrar-E(Proff.)

Leo V. Macedo

Controller Of Examinations

Prof. V.P. Kamat Registrar

